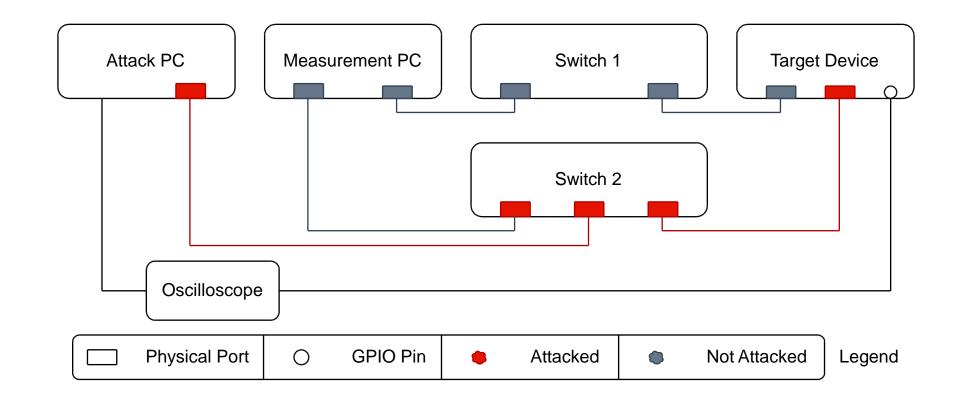
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Robustness Testing for Embedded Devices Against DoS Attacks

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Motivation

Denial-of-service (DoS) attacks have garnered significant attention in both industry and research for decades due to their capacity to inflict damage using relatively simplistic methods and minimal expertise. However, the topic is mainly discussed in relation to the Internet and network level technologies as well as use cases. It is important to note that Industrial Control System components, such as Programmable Logic Controller and other real-time devices, are susceptible to DoS attacks as well. This is demonstrated in research scenarios like the malware PLC-Blaster [1] and in practical instances such as Industroyer [2].



Currently, no best practice against DoS attacks on embedded devices seem to exist. This research is part of the "Digital Sensor-2-Cloud Campus Platform" (DS2CCP) project [3], which aims to demonstrate reliable communication between the industrial shop floor and the edge cloud. The main goal is to provide a set of best practice methods as well as solutions to increase the resilience of embedded devices against network based DoS attacks.

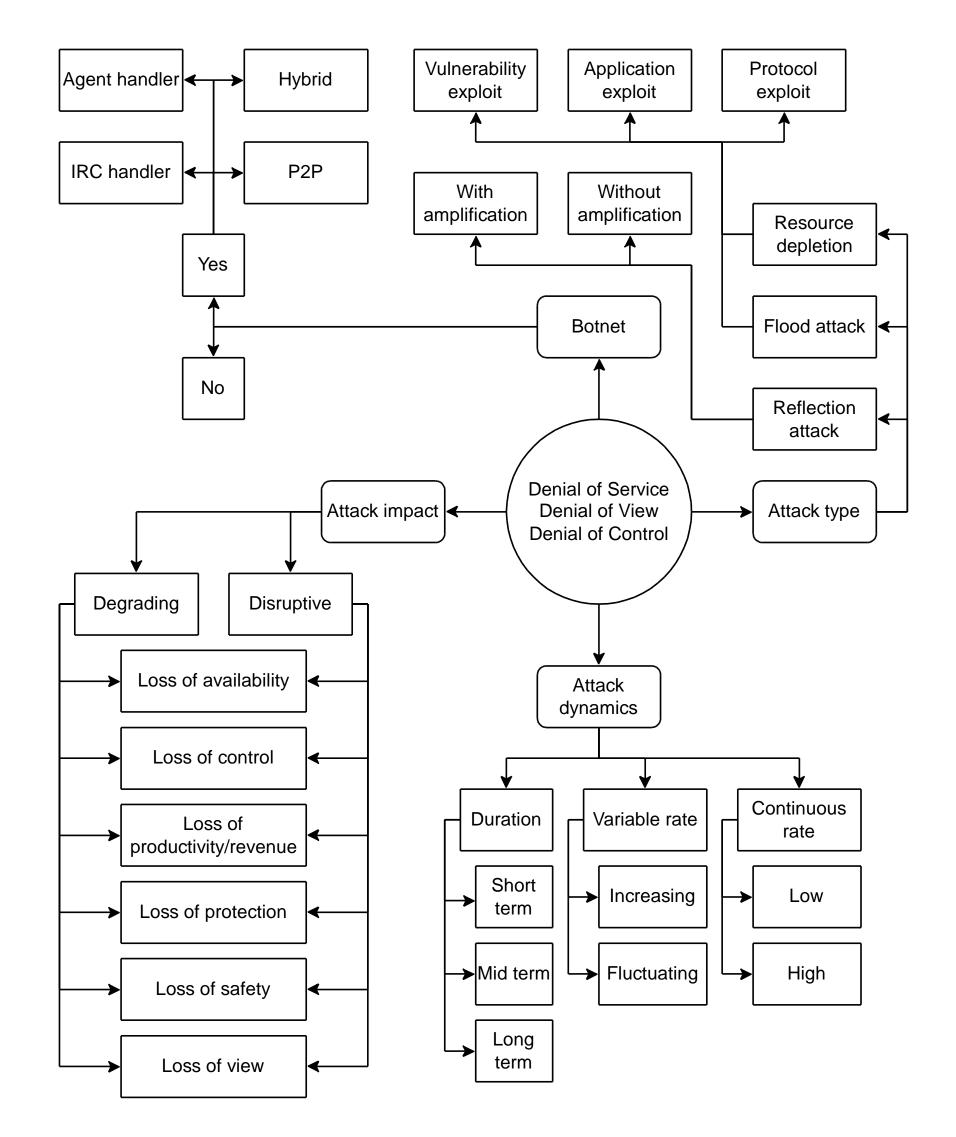


Fig. 2. Proposed Measurement Setup Scheme for Robustness Testing of Embedded Devices against DoS attacks

Robustness Testing of Embedded Devices

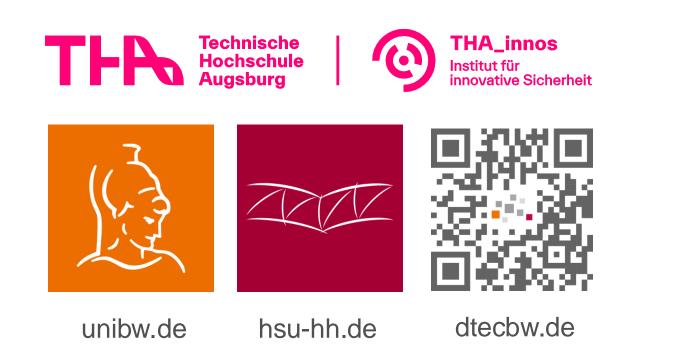
Testing embedded devices for their robustness against DoS attacks cannot be done the way most DoS testbeds are designed today: testing the impact on the whole network at once. Instead, only the impact of the DoS attack on the attacked device itself should evaluated when discussing device robustness. Thus, some precautions have to be made. The proposed approach consists of two elements. Firstly, using a redundant network topology to allow reference communication over infrastructure suffering attack and infrastructure not suffering an attack. The other part of the approach consists of evaluating the Quality of Service of essential functions of the device as well as detailed profiling of the victim device itself.

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Fig. 1. Taxonomy of DoS attacks based on [4] and [5]





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